IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	Confirmation No.: **6438**
Young-Joon SONG	Group Art Unit: **2663**
Patent No.: **6,721,299 B1** Issued: **April 13, 2005**	Examiner: **NHAT Q. DO**
Serial No.: **09/376,373** Filed: **August 18, 1999**	Customer No.: **34610**

For: **PILOT SIGNALS FOR SYNCHRONIZATION AND/OR CHANNEL ESTIMATION**

REQUEST FOR CERTIFICATE OF CORRECTION

U.S. Patent and Trademark Office
Attention: **CERTIFICATE OF CORRECTION BRANCH**
Customer Service Window
Randolph Building
401 Dulany Street
Alexandria, Virginia 22314

Sir:

Applicants request the U.S. Patent and Trademark Office to issue a Certificate of Correction for **claim 36** of Issued Patent No. **6,721,299** to correct an error made by the Patent Office during printing.

Claim 36 is marked to show changes as to where the error occurred:

36.   A frame structure for a downlink Dedicated Physical Control Channel (DPCCH) in a communication system, wherein the improvement comprises each frame of the downlink DPCCH having 15 slots and N_{pilot} number of pilot bits in each slot, where $2 \leq N_{pilot} \leq 16$, and pilot bit patterns comprise at least one of the following based on N_{pilot} number of pilot bits:

Symbol #	when Npilot = 2	when Npilot = 4		when Npilot = 8				when Npilot = 16							
	0	0	1	0	1	2	3	0	1	2	3	4	5	6	7
Slot #0	11	11	11	11	11	11	10	11	11	11	10	11	11	11	10
1	00	11	[[10]] 00	11	00	11	10	11	00	11	10	11	11	11	00
2	01	11	01	11	01	11	01	11	01	11	01	11	10	11	00
3	00	11	00	11	00	11	00	11	00	11	00	11	01	11	10
4	10	11	10	11	10	11	01	11	10	11	01	11	11	11	11
5	11	11	11	11	11	11	10	11	11	11	10	11	01	11	01
6	11	11	11	11	11	11	00	11	11	11	00	11	10	11	11
7	10	11	10	11	10	11	00	11	10	11	00	11	10	11	00
8	01	11	01	11	01	11	10	11	01	11	10	11	00	11	11
9	11	11	11	11	11	11	11	11	11	11	11	11	00	11	11
10	01	11	01	11	01	11	01	11	01	11	01	11	11	11	10
11	10	11	10	11	10	11	11	11	10	11	11	11	00	11	10
12	10	11	10	11	10	11	00	11	10	11	00	11	01	11	01
13	00	11	00	11	00	11	11	11	00	11	11	11	00	11	00
14	00	11	00	11	00	11	11	11	00	11	11	11	10	11	01

wherein pilot bit patterns are used in a communication system.

This correction is supported by the Reply filed on April 11, 2003 (printed claim 36 corresponds to claim 37 in the April 11, 2003 Reply).

It is believed that no fee is due. However, please credit or debit Deposit Account No. 16-0607 as necessary to effect entry of the attached corrections.

Respectfully submitted,
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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : **6,721,299 B1**

APPLICATION NO.: **09/376,373**

ISSUE DATE : **April 13, 2004**

INVENTOR(S) : **Young-Joon SONG**

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

36.   A frame structure for a downlink Dedicated Physical Control Channel (DPCCH) in a communication system, wherein the improvement comprises each frame of the downlink DPCCH having 15 slots and N_{pilot} number of pilot bits in each slot, where $2 \leq N_{pilot} \leq 16$, and pilot bit patterns comprise at least one of the following based on N_{pilot} number of pilot bits:

	when $N_{pilot} = 2$	when $N_{pilot} = 4$		when $N_{pilot} = 8$				when $N_{pilot} = 16$							
Symbol #	0	0	1	0	1	2	3	0	1	2	3	4	5	6	7
Slot #0	11	11	11	11	11	11	10	11	11	11	10	11	11	11	10
1	00	11	00	11	00	11	10	11	00	11	10	11	11	11	00
2	01	11	01	11	01	11	01	11	01	11	01	11	10	11	00
3	00	11	00	11	00	11	00	11	00	11	00	11	01	11	10
4	10	11	10	11	10	11	01	11	10	11	01	11	11	11	11
5	11	11	11	11	11	11	10	11	11	11	10	11	01	11	01
6	11	11	11	11	11	11	00	11	11	11	00	11	10	11	11
7	10	11	10	11	10	11	00	11	10	11	00	11	10	11	00
8	01	11	01	11	01	11	10	11	01	11	10	11	00	11	11
9	11	11	11	11	11	11	11	11	11	11	11	11	00	11	11
10	01	11	01	11	01	11	01	11	01	11	01	11	11	11	10
11	10	11	10	11	10	11	11	11	10	11	11	11	00	11	10
12	10	11	10	11	10	11	00	11	10	11	00	11	01	11	01
13	00	11	00	11	00	11	11	11	00	11	11	11	00	11	00
14	00	11	00	11	00	11	11	11	00	11	11	11	10	11	01

wherein pilot bit patterns are used in a communication system.

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